Linear Laser Driver V2 (LLD2)

Description:

The laser driver converts a differential input voltage into a single ended output current superimposed on a pre-settable constant pre-bias. The 3 channels can be individually programmed via an I2C interface, allowing channel masking and gain/pre-bias control. Input voltage levels are compatible to digital LVDS standard, but the driver is optimized for analogue operation.
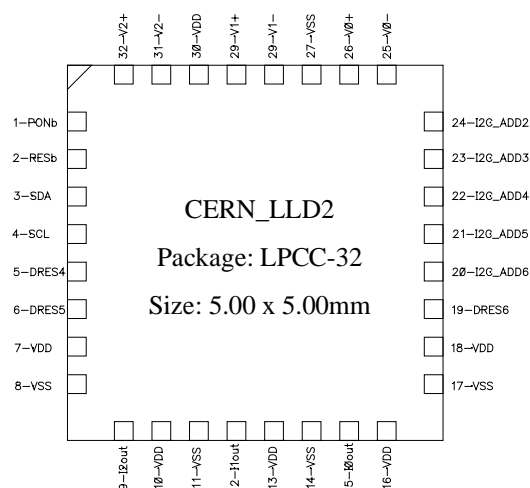
Features

- 0.25µm CMOS (2.5V)
- Package: LPCC-32 (5.00 x 5.00 mm)
- Modularity: 3 channels
- Fully differential architecture
- Biasing scheme: current sink
- Linearity 1%
- I2C interface (redundant logic)
- Programmable gain/pre-bias
- On-chip pre-bias generation (0-55mA)
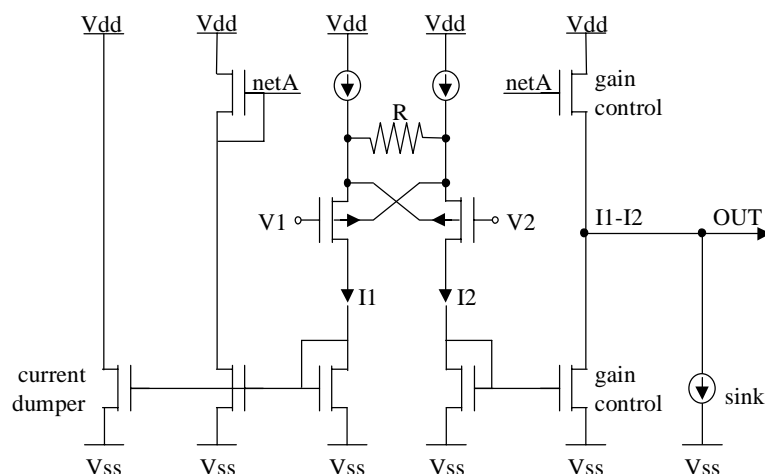- Individual channel masking

Block diagram

Bonding diagram

CERN_LLD2

Package: LPCC-32
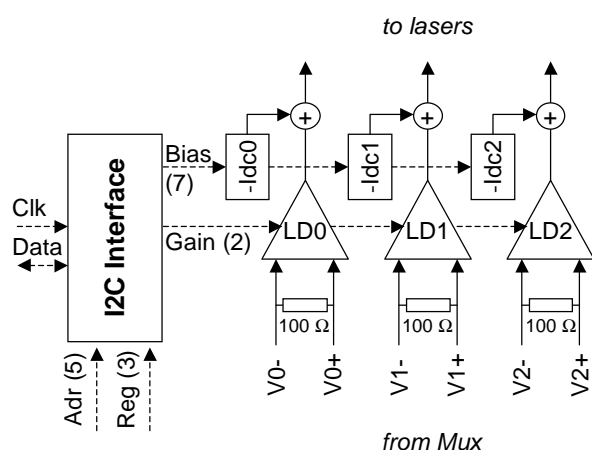
Size: 5.00 x 5.00mm

Simplified schematic (analog block)

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